

## LCDA12C-8 & LCDA15C-8 Low Capacitance TVS Diode Array For Multi-mode Transceiver Protection

### **PROTECTION PRODUCTS**

### Description

The LCDAxxC-8 has been specifically designed to protect sensitive components which are connected to data and transmission lines from over voltages caused by **ESD** (electrostatic discharge), **EFT** (electrical fast transients), and **lightning**.

The low capacitance array configuration of the LCDAxxC-8 allows the user to protect eight high-speed data or I/O lines. They may be used on systems operating from 5 to 15 Volts. The high surge capability (500W, tp=8/20µs) makes the LCDAxxC-8 suitable for telecommunications systems operating in harsh transient environments. The low inductance construction minimizes voltage overshoot during high current surges.

The features of the LCDAxxC-8 are ideal for protecting multi-protocol transceivers in WAN applications such as Frame Relay systems, routers, and switches.

#### Features

- Transient protection for high-speed data lines to IEC 61000-4-2 (ESD) 15kV (air), 8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 0.5kV, 12A (8/20µs)
- Protects eight I/O lines
- Low capacitance for high-speed interfaces
- High surge capability
- Low clamping voltage
- Solid-state silicon avalanche technology

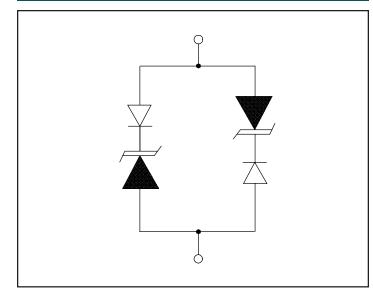
#### **Mechanical Characteristics**

- ◆ JEDEC SO-16 package
- Molding compound flammability rating: UL 94V-0
- Marking : Part number, date code
- Packaging : Tape and Reel per EIA 481

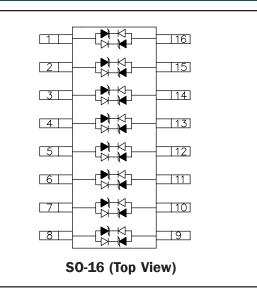
#### Applications

- ◆ Multi-Mode Transceiver Protection
- WAN Equipment: CSU/DSU Multiplexers Routers ISP Equipment Customer Premise Equipment
  Protection for any of the following
  - Protection for any of the following interfaces: RS-232 (V.28) RS-422 (V.11, X.21)
    - RS-449 (V.11/V.10)

### Circuit Diagram



### Schematic & PIN Configuration





### **PROTECTION PRODUCTS**

Absolute Maximum Rating

| Rating                                 | Symbol           | Value         | Units |
|--|------------------|---------------|-------|
| Peak Pulse Power ( $t_p = 8/20\mu s$ ) | P <sub>pk</sub>  | 500           | Watts |
| Lead Soldering Temperature             | T                | 260 (10 sec.) | °C    |
| Operating Temperature                  | T,               | -55 to +125   | °C    |
| Storage Temperature                    | T <sub>stg</sub> | -55 to +150   | °C    |

### **Electrical Characteristics**

| LCDA12C-8                  |                  |   |         |         |         |       |
|----------------------------|------------------|---|---------|---------|---------|-------|
| Parameter                  | Symbol           | Conditions  | Minimum | Typical | Maximum | Units |
| Reverse Stand-Off Voltage  | V <sub>RWM</sub> |   |         |         | 12      | V     |
| Reverse Breakdown Voltage  | V <sub>BR</sub>  | I <sub>t</sub> = 1mA                                    | 13.3    |         |         | V     |
| Reverse Leakage Current    | I <sub>R</sub>   | V <sub>RWM</sub> = 12V, T=25°C                          |         |         | 5       | μA    |
| Clamping Voltage           | V <sub>c</sub>   | $I_{pp} = 5A, t_p = 8/20 \mu s$                         |         |         | 19      | V     |
| Clamping Voltage           | V <sub>c</sub>   | $I_{pp} = 20A, t_p = 8/20\mu s$                         |         |         | 26.6    | V     |
| Maximum Peak Pulse Current | l ad             | t <sub>p</sub> = 8/20µs                                 |         |         | 20      | А     |
| Junction Capacitance       | C <sub>j</sub>   | Between I/O pins & Gnd<br>$V_{_{\rm R}}$ = OV, f = 1MHz |         | 8       | 15      | pF    |

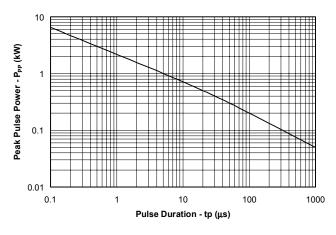
| LCDA15C-8                  |                  |   |         |         |         |       |
|----------------------------|------------------|---|---------|---------|---------|-------|
| Parameter                  | Symbol           | Conditions  | Minimum | Typical | Maximum | Units |
| Reverse Stand-Off Voltage  | V <sub>RWM</sub> |   |         |         | 15      | V     |
| Reverse Breakdown Voltage  | V <sub>BR</sub>  | I <sub>t</sub> = 1mA                              | 16.7    |         |         | V     |
| Reverse Leakage Current    | I <sub>R</sub>   | V <sub>RWM</sub> = 15V, T=25°C                    |         |         | 5       | μA    |
| Clamping Voltage           | V <sub>c</sub>   | $I_{pp} = 1A, t_p = 8/20 \mu s$                   |         |         | 24      | V     |
| Clamping Voltage           | V <sub>c</sub>   | $I_{pp} = 15A, t_p = 8/20\mu s$                   |         |         | 33      | V     |
| Maximum Peak Pulse Current | I <sub>pp</sub>  | t <sub>p</sub> = 8/20µs                           |         |         | 15      | A     |
| Junction Capacitance       | C <sub>j</sub>   | Between I/O pins & Gnd<br>$V_{R} = 0V$ , f = 1MHz |         | 8       | 15      | pF    |



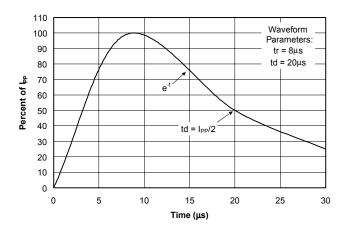
## PROTECTION PRODUCTS

### **Typical Characteristics**

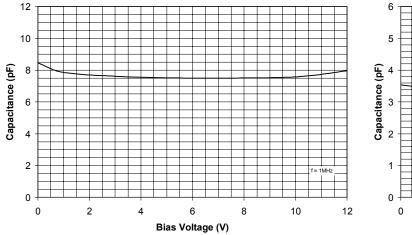
#### Non-Repetitive Peak Pulse Power vs. Pulse Time



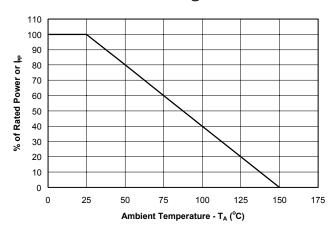
**Pulse Waveform** 



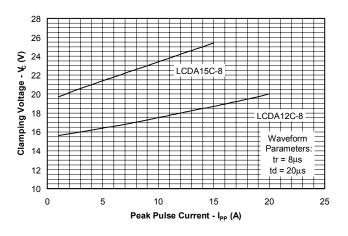
LCDA12C-8 Typical Capacitance vs. Reverse Voltage



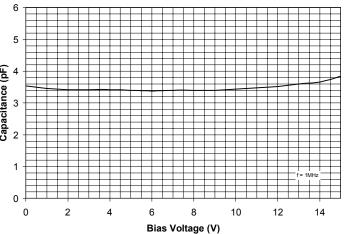
**Power Derating Curve** 



**Clamping Voltage vs. Peak Pulse Current** 



LCDA15C-8 Typical Capacitance vs. Reverse Voltage





# PROTECTION PRODUCTS

### Applications Information

#### Device Connection Options for Protection of Eight High-Speed Data Lines

The LCDAxxC-8 may be configured to protect up to eight I/O lines operating between 5 and 15V. It may be used to protect the most popular serial data interface standard lines making it ideal for use in equipment utilizing multi-mode transceivers. The LCDAxxC-8 is symmetrical so the data lines may be connected at pins 1-8 or 9-16. Pins 9-16 or 1-8 are connected to ground as shown. For best results, these pins should be connected directly to a ground plane on the board. The path length should be kept as short as possible to minimize parasitic inductance.

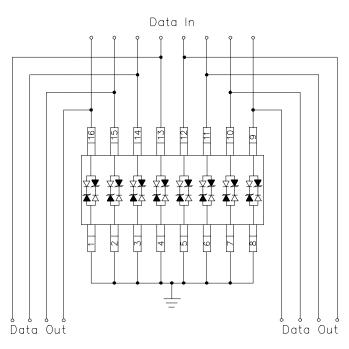
#### **Multi-Mode Transceiver Protection**

The LCDAxxC-8 may be used to protect multi-mode transceiver I/O lines with external connections. The LCDAxxC-8 adds a maximum loading capacitance of 15pF with a working voltage of 12V or 15V. This allows the transceiver to safely operate in all modes without clipping or degradation of the signal. With proper design and layout, the transceiver port can be protected to >15kV (HBM per IEC 61000-4-2).

# **Circuit Board Layout Recommendations for Suppression of ESD.**

Good circuit board layout is critical for the suppression of fast rise-time transients such as ESD. The following guidelines are recommended:

- Place the LCDAxxC-8 near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the LCDAxxC-8 and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

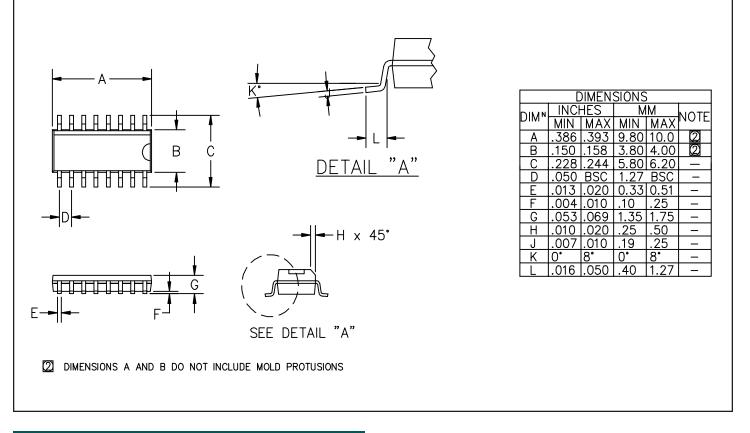


#### **Device Connection**

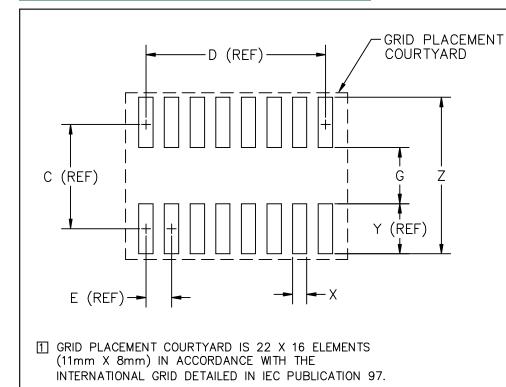


## PROTECTION PRODUCTS

### Outline Drawing - SO-16



### Land Pattern - SO-16



|      | DIMENSIONS |      |      |      |      |  |
|------|------------|------|------|------|------|--|
|      | INCHES     |      | MM   |      | NOTE |  |
| DIM™ | MIN        | MAX  | MIN  | MAX  | NUTE |  |
| С    | _          | .197 | _    | 5.00 | _    |  |
| D    | -          | .35  | -    | 8.89 | -    |  |
| E    | -          | .05  | -    | 1.27 |      |  |
| G    | .102       | .110 | 2.60 | 2.80 |      |  |
| Х    | .02        | .03  | .60  | .80  |      |  |
| Y    | 1          | .095 |      | 2.40 | 1    |  |
| Z    | .28        | .29  | 7.20 | 7.40 | -    |  |



# PROTECTION PRODUCTS

## Ordering Information

| Part Number  | Working<br>Voltage | Qty per<br>Reel | Reel Size |
|--------------|--------------------|-----------------|-----------|
| LCDA12C-8.TE | 12V                | 2,500           | 13 Inch   |
| LCDA15C-8.TE | 15V                | 2,500           | 13 Inch   |

Note:

(1) No suffix indicates tube pack.

### Contact Information

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